

Standalone 1A Linear Lithium Battery Charger With Thermal Regulation

INTRODUCTION

The SMC4010 is a complete constant-current/ constant-voltage linear charger for single cell lithium rechargeable battery. No external sense resistor is needed, and no blocking diode is required due to the internal P-MOSFET architecture. Furthermore, the SMC4010 is specifically designed to work within USB power specifications. Its low external component count makes the SMC4010 ideally suited for portable applications. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge current can be programmed externally with a single resistor. The SMC4010 automatically terminates the charge cycle when the charge current drops to 1/10th the programmed value after the final float voltage is reached. When the input supply (wall adapter or USB supply) is removed, the SMC4010 automatically enters a low power sleep mode, dropping the battery drain current to less than 2µA. The SMC4010 can be put into shutdown mode, reducing the supply current to 50µA. Other features include battery pack temperature monitor, undervoltage lockout, automatic recharge and two status pins to indicate charging and charge termination. The SMC4010 is available in thermally enhanced SOP8-PP package.

■ FEATURES

- Charges Single Cell Lithium Battery Directly from USB Port or AC Adapter
- Input Voltage Range From 4.5V to 6.5V
- No External MOSFET, Sense Resistor or Blocking Diode Required
- Preset 4.20V/4.35V Charge Voltage
- Continuous Programmable Charge Current Up to 1A
- Precharge Conditioning for Reviving Deeply Discharged Cells and Minimizing Heat Dissipation During Initial Stage of Charge
- Constant-Current/Constant-Voltage/Constant

 Temp Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- Charge Termination: SMC4010A, C/10 SMC4010B, C/4
- Automatic Recharge
- Battery Temperature Sensing
- Charge state pairs of output, no battery and fault status display
- Charge Current Monitor Output for Gas Gauging
- Automatic Low Power Sleep Mode When Input Supply Voltage is Removed
- Soft-Start Limits Inrush Current
- Chip Enable Input

APPLICATIONS

- Cellular phones, PDAs
- Portable Media Players
- Digital Still Cameras

- Bluetooth & GPS Applications
- Mobile Internet Device
- Charging Docks and Cradles

■ ORDER INFORMATION

Device No.	Battery Float Voltage	Charge Termination	Package	Packaging
SMC4010A420ES	4.20V	C/10	SOP8-PP	3000 parts per reel
SMC4010A435ES	4.35V	C/10	SOP8-PP	3000 parts per reel
SMC4010B420ES	4.20V	C/4	SOP8-PP	3000 parts per reel
SMC4010B435ES	4.35V	C/4	SOP8-PP	3000 parts per reel

■ PIN CONFIGURATION

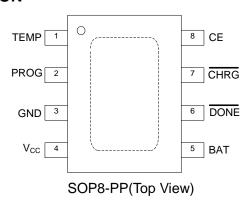
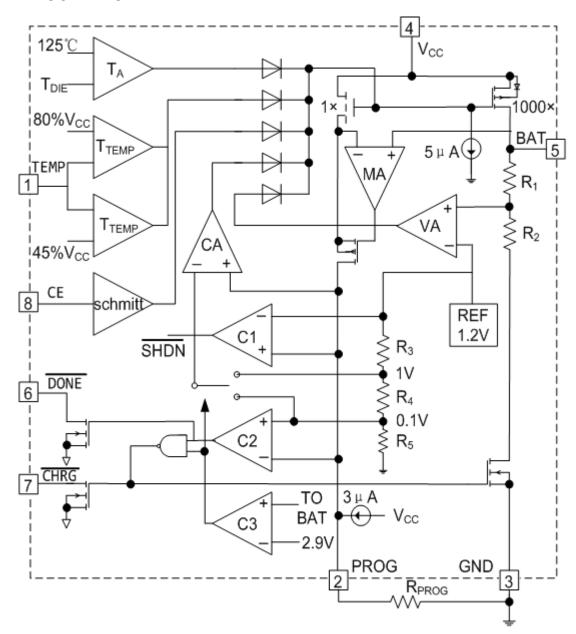


Table 1. Pin Description

	Description	FUNCTION		
PIN NO.	PIN NAME	FUNCTION		
1	TEMP	Battery temperature detection input. Connecting TEMP pin to NTC thermistor's sensor output in Lithium ion battery pack. If the TEMP pin's voltage is less than 45% or greater than 80% of the input voltage V _{CC} , this means the battery temperature is too high or too low, charging is suspended. If the TEMP pin's voltage level is between 45% and 80% of the input voltage V _{CC} , battery fault state is released, and charging will resume. If the TEMP pin direct access GND, battery temperature detection canceled, the other charged functioning properly.		
2	PROG	Constant Charge Current Setting and Charge Current Monitor Pin. The charge current is set by connecting a 1% accuracy metal film resistor R _{PROG} from this pin to GND. When charging in precharge mode, the PROG pin voltage is regulated to 0.1V. When charging in constant-current mode, the PROG pin voltage is regulated to 1V.In all modes during charging, the voltage on PROG pin can be used to measure the charge current as the following formula: I _{BAT} =(V _{PROG} /R _{PROG}) X 1000.		
3	GND	Ground Terminal.		
4	Vcc	Positive Input Supply Voltage . V_{CC} is the power supply to the internal circuit. V_{CC} can range from 4.5V to 6.5V and should be bypassed with at least a 4.7 μ F capacitor. When V_{CC} drops to within 80mv of the BAT pin voltage,		

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		SMC4010 enters low power sleep mode, dropping BAT pin's current to less
		than 2μA.
		Charger Power Stage Current Output and Battery Voltage Sense Input.
		BAT pin provides charge current to the battery and regulates the final float
		voltage. An internal precision resistor divider from this pin sets the float
5	BAT	voltage which is disconnected in shutdown mode. Connect the positive
		terminal of the battery to BAT pin. Bypass BAT to GND with 10μF to 47μF
		capacitor. BAT pin draws less than 2µA current in chip disable mode or in
		sleep mode.
		Open-Drain Charge termination Status Output. In charge termination
6	DONE	status, DONE is pulled low by an internal switch; Otherwise DONE pin is in
		high impedance state.
		Open Drain Charge Status Output. When the battery is being charged, the
7	CHRG	CHRG pin is pulled low by an internal switch, otherwise CHRG pin is in high
,	CHING	pin is pulled low by an internal switch, otherwise critical pin is in high
		impedance state.
		Chip Enable Input. A high input will put the device in the normal operating
Q	CE	mode. Pulling the CE pin to low level will put the SMC4010 into disable
0	CL	mode. The CE pin can be driven by TTL or CMOS logic level. The CE pin is
		high impedance with internal 1.1M Pull-up Resistor in the suspended state.
	Thormal	Exposed Paddle (bottom). This pin should be soldered to the PCB ground
EP		as close as to the device for electrical contact and rated thermal
	PAD	performance.
EP	CE Thermal PAD	high impedance with internal 1.1M Pull-up Resistor in the suspended state. Exposed Paddle (bottom). This pin should be soldered to the PCB ground as close as to the device for electrical contact and rated thermal

■ BLOCK DIAGRAM



Future 1 Functional Block Diagram

■ ABSOLUTE MAXIMUM RATINGS(1)

(Unless otherwise specified, $T_A = 25^{\circ}C$)

PARAMETER	SYMBOL	RATINGS	UNITS
Input Supply Voltage(2)	V _{CC}	-0.3 ~ 10	
TEMP, CE, PROG Pins Voltage ⁽²⁾		-0.3 ~ V _{CC} + 0.3	V
BAT Pin Voltage ⁽²⁾		-0.3 ~ 8	V
CHRG, DONE Pins Voltage(2)		-0.3 ~ 10	
BAT Short-Circuit Duration	-	Continuous	-
BAT Pin Output Current (Continuous)	I _{BAT}	1200	mA
Output sink current	ICHRG, IDONE	10	mA
Power dissipation	P _D	1500	mW
Operating Ambient Temperature	T _A	-40 ~ 85	ο̈́
Range ⁽³⁾		-40 ~ 03	C
Junction Temperature	TJ	-40 ~ 150	°C
Storage Temperature	T _{stg}	-40 ~ 125	°C
Lead Temperature (Soldering, 10s)	T _{solder}	260	°C
ESD rating ⁽⁴⁾	HBM	2000	V
ESD fatting(*)	MM	200	V

- (1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods my affect device reliability.
- (2) All voltages are with respect to network ground terminal.
- (3) The SMC4010 are guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.
- (4) The human body model is a 100pF capacitor discharged through a $1.5k\Omega$ resistor into each pin. The machine model is a 200pF capacitor discharged directly into each pin.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	MAX	UNITS
Input voltage range ⁽⁵⁾	V _{CC}	4.5	6.5	V
BAT Pin Output Current (Continuous)	I _{BAT}		1000(6)	mA
Fast-charge current programming resistor ⁽⁷⁾	R _{PROG}	1	10	kΩ

- (5) If V_{CC} is between UVLO and 4.5V, and above the battery voltage, then the IC is active (can deliver some charge to the battery), but the IC will have limited or degraded performance (some functions may not meet data sheet specifications). The battery may be undercharged (V_{FLOAT} less than in the specification), but will not be overcharged (V_{FLOAT} will not exceed specification).
- (6) The thermal regulation feature reduces charge current if the IC's junction temperature reaches 125°C; Thus without a good thermal design the maximum programmed charge current may not be reached.
- (7) Use a 1% tolerance metal film resistor for R_{PROG} to avoid issues with the R_{PROG} short test when using the maximum charge current setting.

■ ELECTRICAL CHARACTERISTICS

($V_{CC} = 5V$, $T_A = 25$ °C, Test Circuit Figure2, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
V _{CC} Under voltage Lockout Threshold	V _{UVL}	V _{CC} from Low to High		3.9		V	
V _{CC} Under voltage Lockout Hysteresis	ΔV_{UVL}			150		mV	
		Charge Mode, R _{PROG} = 10K		150	500	- μΑ	
Input Supply Current	Icc	Standby Mode (Charge Terminated)		50	100		
		Shutdown Mode: R_{PROG} Not Connected, $V_{CC} < V_{BAT}$, or $V_{CC} < V_{UVL}$		50	100	μ	
CE "High" Level Voltage	V _{CEH}		1.5		Vcc	V	
CE "Low" Level Voltage	V _{CEL}				0.4	V	
Trickle Charge Threshold	V _{TRIKL}	R _{PROG} = 10K, V _{BAT} Rising		2.9		V	
Trickle Charge Hysteresis	ΔV_{TRIKL}	R _{PROG} = 10K		100		mV	
Triakla Chargo Current	1	R _{PROG} = 1K, For SMC4010 A	90	100	110	mA	
Trickle Charge Current	I _{TRIKL}	R _{PROG} = 1K, For SMC4010 B	225	250	275	mA	
	I _{BAT}	$R_{PROG} = 1K$, Current Mode($V_{BAT} = 4.0V$)	900	1000	1100	- mA	
		$R_{PROG} = 2K$, Current Mode($V_{BAT} = 4.0V$)	450	500	550		
BAT Pin Current		Standby Mode, V _{BAT} = V _{FLOAT}	0	-2.5	-6.0		
		Shutdown Mode (R _{PROG} Not Connected)		±1	±2	μΑ	
		Sleep Mode, V _{CC} = 0V		-1	-2		
PROG Pin Voltage	V_{PROG}	R _{PROG} = 1K, Current Mode	0.9	1.0	1.1	V	
PROG Pin Pull-Up Current	I _{PROG}			3		μΑ	
Regulated Output	V	$0^{\circ}\text{C} \le \text{T}_{\text{A}} \le 85^{\circ}\text{C},$	4.158	4.200	4.250	V	
(Float) Voltage	V_{FLOAT}	$I_{BAT} = 20 \text{mA}, R_{PROG} = 10 \text{K}$	4.300	4.350	4.400	V	
C/10 Termination	l	R _{PROG} = 1K, For SMC4010 A		0.1		mA/mA	
Current Threshold	I _{TERM}	R _{PROG} = 1K, For SMC4010 B		0.25		mA/mA	
Termination Comparator Filter Time	t _{TERM}	I _{BAT} Falling Below I _{TERM}	0.3	0.8	2.0	mS	
Recharge Battery Threshold	$\triangle V_{RECHG}$	V _{FLOAT} - V _{RECHG}		150		mV	
Recharge Comparator Filter Time	trecharge	V _{BAT} High to Low	0.3	0.8	2.0	mS	

ELECTRICAL CHARACTERISTICS(continued)

(V_{CC} = 5V, T_A = 25°C, Test Circuit Figure2, unless otherwise specified)

V _{CC} - V _{BAT} Lockout	A _{MSD}	V _{CC} from Low to High		100	-	mV
Threshold		V _{CC} from High to Low		80		mV
CHRG Pin Voltage	VCHRG	ICHRG = 5mA		0.3	0.6	V
DONE Pin Voltage	VDONE	IDONE = 5mA		0.3	0.6	V
TEMP High Shift			76	80	82	
Voltage Level			70	80	02	%V _{cc}
TEMP Low Shift			43	45	49	70 V CC
Voltage Level			43	45	49	
Soft-Start Time	t _{SS}	$I_{BAT} = 0$ to $I_{BAT} = 1000V/R_{PROG}$		20		μS
Power FET "ON"						
Resistance (Between V _{CC}	Ron	$I_{BAT} = 1000 mA$		400		$m\Omega$
and BAT)						
Junction Temperature in					_	
Constant Temperature	$T_{J(REG)}$			125		°C
Mode						

■ TYPICAL APPLICATION CIRCUIT

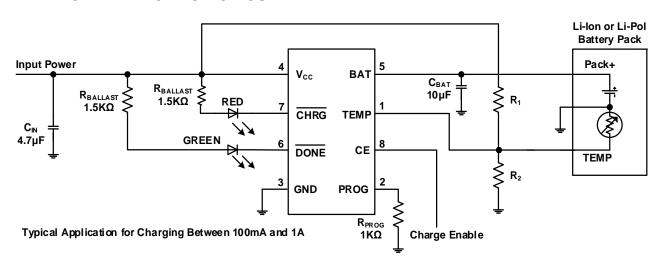


Figure 2 Standard Application Circuit

■ FUNCTIONAL DESCRIPTION

The SMC4010 series are highly integrated Li-Ion or Li-Pol linear battery chargers, targeted at space-limited portable applications. It operates from either a USB port or Wall Adapter and charges a single-cell Li-Ion or Li-Pol battery with up to 1000mA of charge current.

The charge current is programmable using external components (RPROG resistor). The charge

process starts when an external input power is connected to the system, $V_{CC} > V_{UVL}$, $V_{CC} > V_{BAT} + V_{(SLP_EXIT)}$, the charger is enabled by the RPROG resistor connected and the battery voltage is below the recharge threshold, $V_{BAT} < V_{RECHG}$.

When the charger is enabled two control loops modulate the battery switch drain to source impedance to limit the BAT pin current to the programmed charge current value (charge current loop) or to regulate the BAT pin voltage to the programmed charge voltage value (charge voltage loop). If $V_{BAT} < V_{TRIKL}$ (2.9V typical), the BAT pin current is internally set to 1/10th of the programmed fast-charge current value in current regulation mode.

The SMC4010 series provide battery charge status via CHRG & DONE status pins.

CHRG & DONE Pins are internally connected to an N-channel open drain MOSFET.

The open drain status output that is not used should be tied to ground.

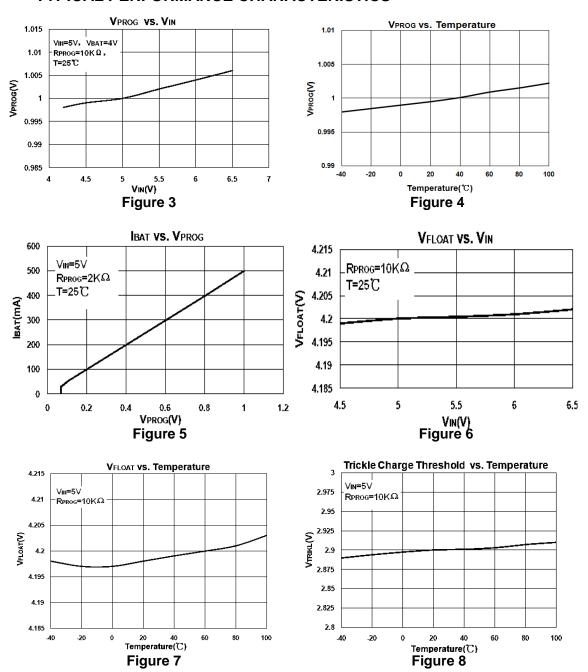
The following table lists the indicator status and its corresponding charging state.

Table 1. Charge Status Indicator⁽¹⁾

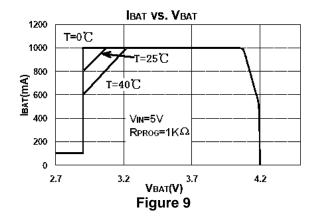
Charge State Description	OHRG	DOME
Preconditioning-Current Mode (Trickle) Charge	ON	HI-Z
Constant-Current Mode (Fast) Charge	ON	HI-Z
Constant-Voltage Mode (Taper) Charge, I _{BAT} > I _{TERM}	ON	HI-Z
Charge Termination (I _{BAT} < I _{TERM} , Charge Done)	HI-Z	ON
Power Down (Undervoltage Lockout) Mode	HI-Z	HI-Z
Sleep Mode	HI-Z	HI-Z
$(V_{UVL} < V_{CC} < V_{BAT} + V_{(SLP_EXIT)}, or the V_{CC} is removed)$	∏I-Z	ПІ-Д
Shutdown Mode(PROG pin floating)	HI-Z	HI-Z
No bottom with Charge Enghlad	FLASH Rate	FLASH
No battery with Charge Enabled	depends on C _{BAT}	FLASH
Fault Condition (Battery Short Circuit)	ON	HI-Z
Fault TEMP(5%V _{CC} < V _{TEMP} < 45%V _{CC} V _{TEMP} > 80%V _{CC})	HI-Z	HI-Z

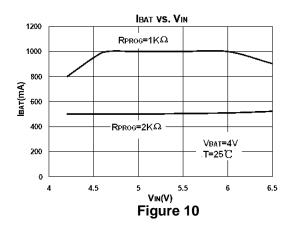
⁽¹⁾ Pulse loading on the BAT pin may cause the IC to cycle between done and charging states (LEDs Flashing)

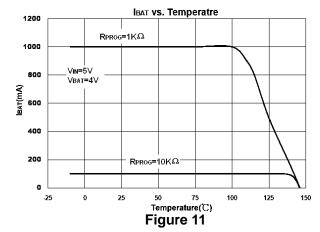
■ TYPICAL PERFORMANCE CHARACTERISTICS

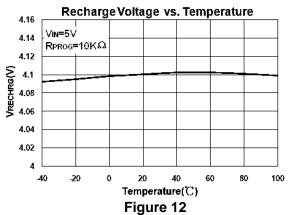


■ TYPICAL PERFORMANCE CHARACTERISTICS(continued)



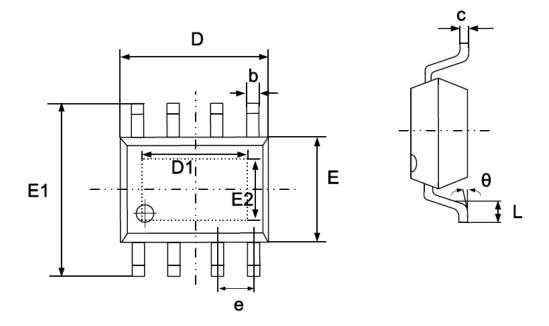


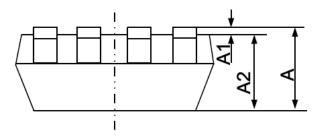




PACKAGING INFORMATION

SOP8-PP Package Outline Dimensions





Symbol	Dimensions In Millimeters		Dimensions	In Inches
	Min.	Max.	Min.	Max.
Α	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
С	0.170	0. 250	0.006	0.010
D	4.700	5.100	0.185	0.200
D1	3.100	3.500	0.122	0.137
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
E2	2.200	2.600	0.086	0.102
е	1.270(BSC)		0.050(BSC)
L	0.400	1.270	0.016	0.050
θ	0°	8°	0 °	8°

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